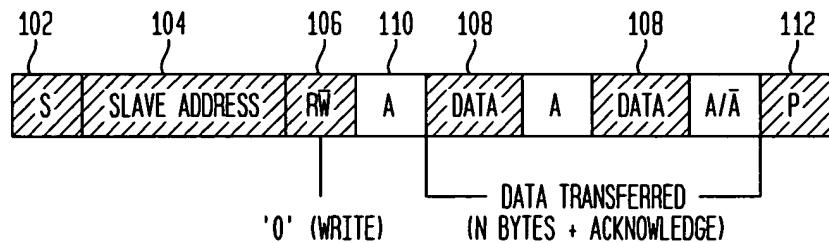




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FIG. 1



FROM MASTER TO SLAVE



FROM SLAVE TO MASTER

A = ACKNOWLEDGE (SDATA LOW)

\bar{A} = NOT ACKNOWLEDGE (SDATA HIGH)

S = START CONDITION

P = STOP CONDITION

FIG. 2

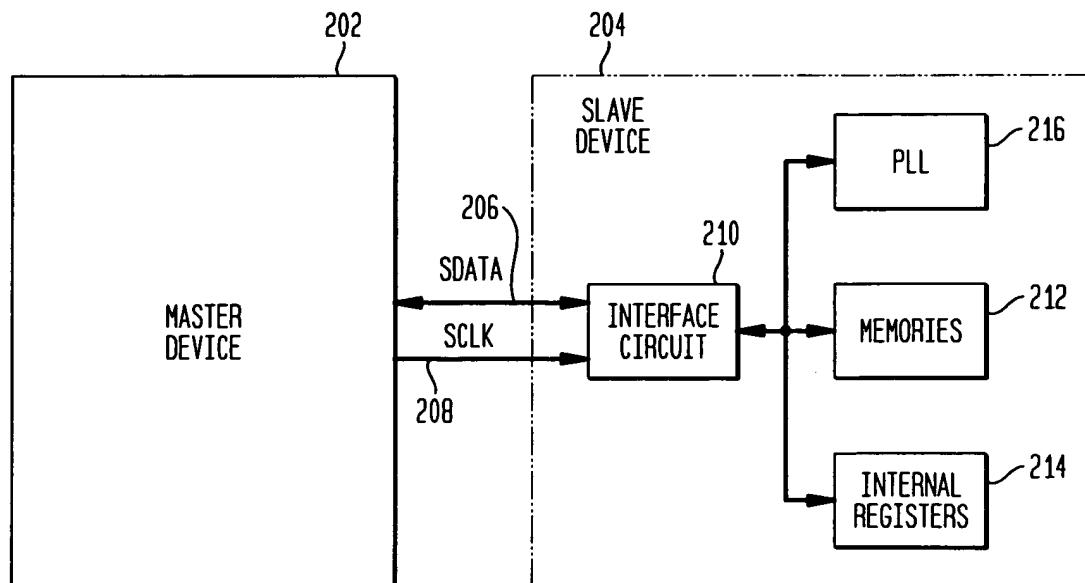
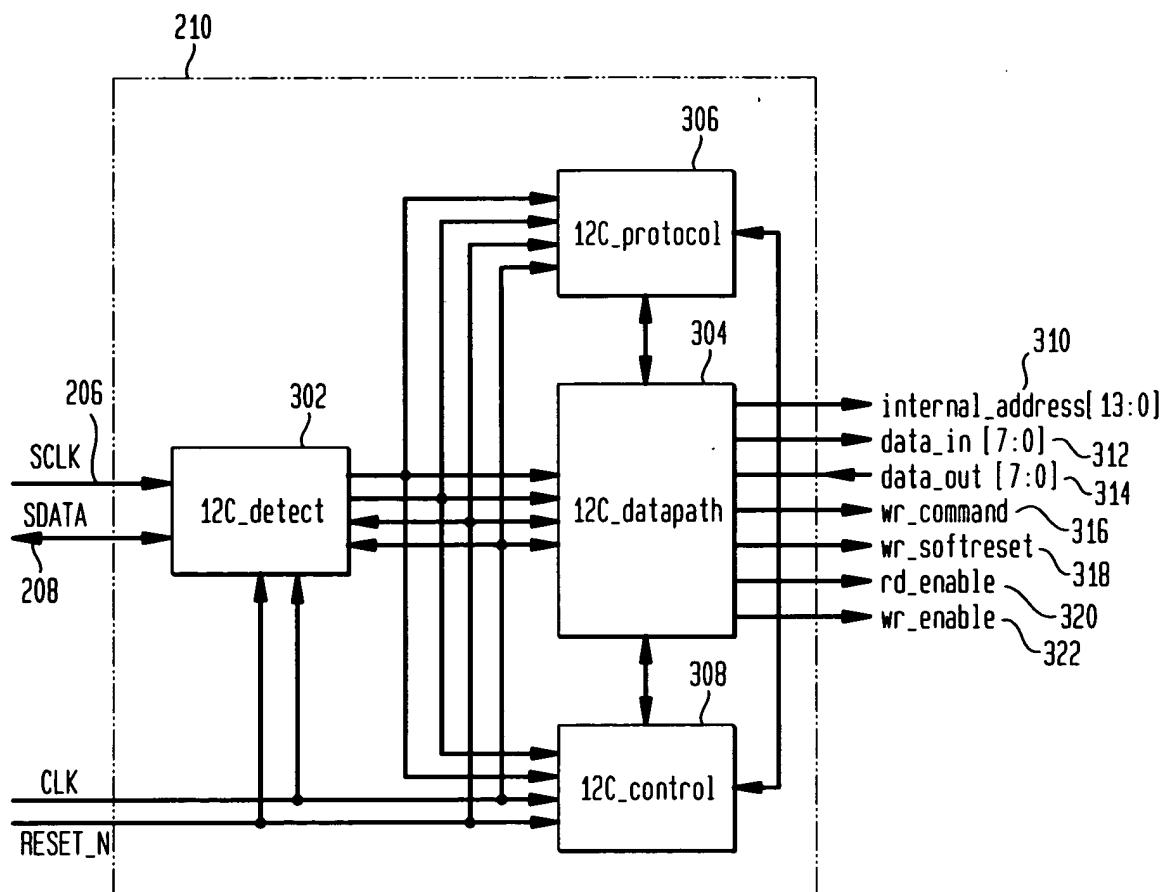


FIG. 3



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FIG. 4

WHERE XXXXXX ARE internal_address[13:8]

102	104	106	110	110	404	108	108	112
START	SLAVE ADDRESS 1st 7 BITS	R/W 0	ACK 0	COMMAND CODE 01xxxxxx	ACK 0	internal address[7:0] 8 BITS	ACK 0	ACK 0
						Data1 5 BITS	Data2 5 BITS	Data3 5 BITS

402

FIG. 5WHERE XXXXXX ARE internal_address[5:0]
AND internal_address[13:8] ARE SET TO ZERO'S

102	104	106	110	402	110	108	110	108	110	112
START	SLAVE ADDRESS 1st 7 BITS	R/W 0	ACK 0	COMMAND CODE 00xxxxxx	ACK 0	Data1 8 BITS	ACK 0	Data2 8 BITS	ACK 0	Data3 8 BITS
						ACK 0	ACK 0	ACK 0	ACK 0	STOP

FIG. 6

WHERE XXXXXX IS THE DESIRED WRITE COMMAND TO SELECTED REGISTERS

102	104	106	110	402	110	112
START	SLAVE ADDRESS 1st 7 BITS	R/W 0	ACK 0	COMMAND CODE 11xxxxxx	ACK	STOP